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SEARCH REQUEST FORM

Scientific and Technical Information Center

EIC 2600

Requester's Full Name Krista Zele Examiner # 67644 Date 9/26/05
Art Unit 2600 Phone Number 2-7288 Serial Number 09/771229
Office Location _____ Format preferred (circle) PAPER EMAIL BOTH
Jeff 2D61

If more than one search is submitted, please prioritize searches in order of need.

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Let us know what you already have and so do not need. Include the keywords, synonyms and meaning of acronyms. Define all terms that may have a specific meaning. Please attach a copy of the background, abstract, claims and other pertinent information.
Please state how the terms or keyword strings should relate to one another.

Title of the Invention _____
Inventor(s) US 5867542

Earliest Priority date to be used _____

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1 / 1 PLUSPAT - ©QUESTEL-ORBIT - image

PN - US5867542 A 19990202 [US5867542]

TI - (A) Clock phase detecting circuit and clock regenerating circuit each arranged in receiving unit of multiplex radio equipment

PA - (A) FUJITSU LTD (JP)

PA0 - Fujitsu Limited, Kanagawa [JP]

IN - (A) IWAMATSU TAKANORI (JP); KIYANAGI HIROYUKI (JP)

AP - US55254395 19951103 [1995US-0552543]

PR - JP5937795 19950317 [1995JP-0059377]

IC - (A) H04L-007/02 H04L-027/22

EC - H04L-007/02V1
H04L-007/033D
H04L-027/38

ICO - T04L-007/027

PCL - ORIGINAL (O) : 375354000; CROSS-REFERENCE (X) : 329304000 375326000 375332000 375344000

DT - Corresponding document

CT - US4320517; US4692931; US4815103; US5090027; US5283780; US5423085; US5471508; US5535252; US5661761

STG - (A) United States patent

AB - The present invention relates to a clock phase detecting circuit and a clock regenerating circuit each arranged in a receiving unit of multiplex radio equipment. The receiving unit of the multiplex radio equipment includes an identifying circuit for identifying a signal obtained by demodulating a multilevel orthogonal modulation signal; a clock regenerating circuit for regenerating a signal identification clock for the identifying circuit to supply the clock to the identifying circuit; an equalizing circuit for subjecting the signal obtained by demodulating a multilevel orthogonal modulation signal to an equalizing process. A clock phase detecting unit detects the phase component of the signal identification clock based on signals input to or output from the equalizing circuit and then supplies the phase component to the clock regenerating circuit. The phase component of a signal identification clock can be certainly detected and accurately adjusted so that the signal identification clock can be regenerated with high accuracy.

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1 / 1 LGST - ©EPO

PN - US5867542 A 19990202 [US5867542]

AP - US55254395 19951103 [1995US-0552543]

ACT -

19951103 US/AS02-A
ASSIGNMENT OF ASSIGNOR'S INTEREST
OWNER: FUJUTSU LIMITED 1015 KAMIKODANAKA, NAKAHARA-KU, KA; EFFECTIVE DATE: 19950821

19951103 US/AS02-A
ASSIGNMENT OF ASSIGNOR'S INTEREST
OWNER: IWAMATSU, TAKANORI; EFFECTIVE DATE: 19950821

19951103 US/AS02-A
ASSIGNMENT OF ASSIGNOR'S INTEREST
OWNER: KIYANAGI, HIROYUKI; EFFECTIVE DATE: 19950818

UP - 2003-22

Search statement 2

LEVEL 1 - 1 OF 1 PATENT

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

5867542

February 2, 1999

Clock phase detecting circuit and clock regenerating circuit each arranged in receiving unit of multiplex radio equipment

APPL-NO: 552543 (08)

FILED-DATE: November 3, 1995

GRANTED-DATE: February 2, 1999

CORE TERMS: clock, phase, detecting, regenerating, identification, conversion, converter, oscillating, detect, input ...

5,687,542 OR 5867542

LEXIS-NEXIS
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File: CASES

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5,687,542 OR 5867542

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October 31, 2003

SECTION: v.13(21) O 31'03; ISSN: 0025-9535

CBCA-ACC-NO: 5867542

LENGTH: 173 words

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Patent Search - Number: 5867542

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